



NOTES:

- 1) All Resistors are SM 0805 package except where noted
- 2) All Non polarity Capacitors are SM 0603 package except where noted
- 3) All polarity Capacitors are A3216 package except where noted
- 4) All Capacitors will be lay out as close as possible to the Probe pins.
- 5) All Resistors will be lay out about 1" from the Probe pins.
- 6) All the double arrows are the original Probe pins number from Circuit Probe Engineering INC
- 7) Differential signal are layout in parallel and on the same plane

Engineer: C . VU
Schematic Layout
Designated Name: _____
Designated Number: _____
Date: _____